

APPROVED	O.G. FIG.	
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FIG. 1A

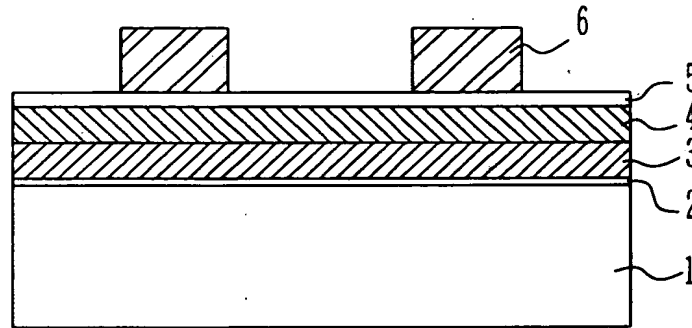


FIG. 1B

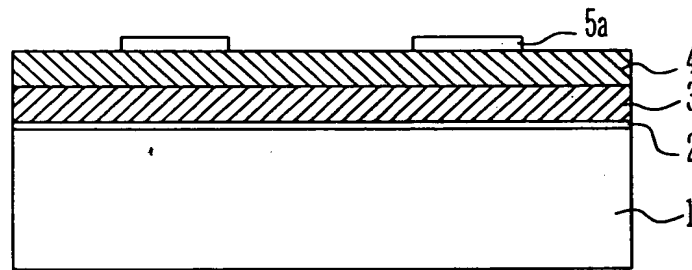


FIG. 1C

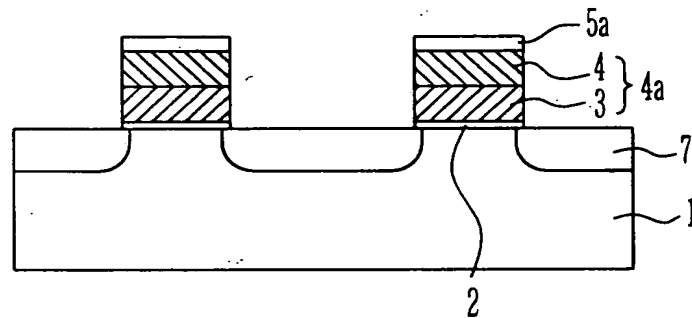
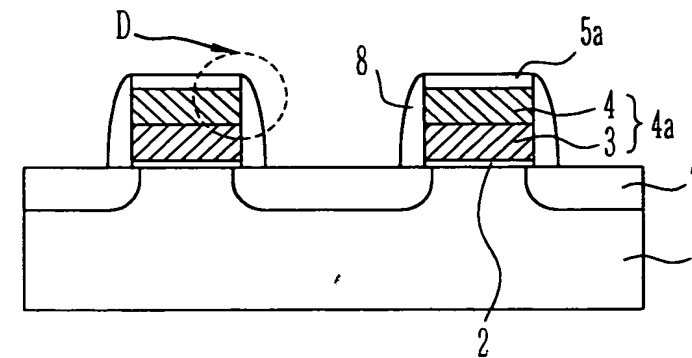


FIG. 1D



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Kim et al. 29936/38063
 "Method of Forming Wiring in
 Semiconductor Devices"
 Sheet 2 of 2 FIGS. 2 & 3

FIG. 2

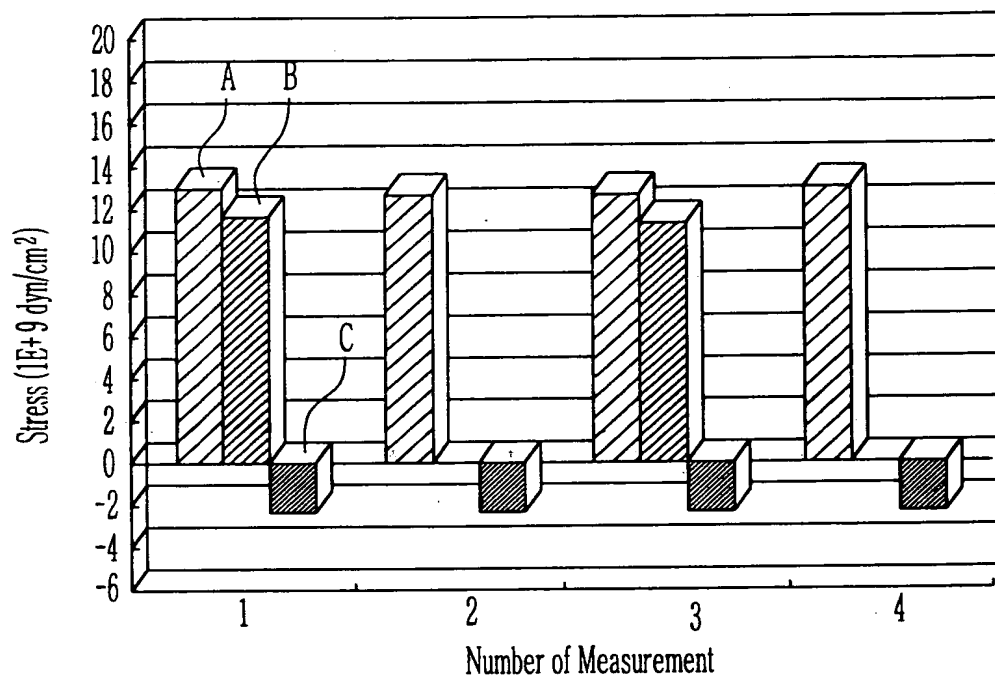


FIG. 3

